Iling Date:  24-Nov-2003  METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER  Akira Matsuda  Jer: Stephen G. Adrian/Larry Payne  1032130  Stephen G. Adrian/Larry Payne  104 Sub-Total in USD(5)  Description Fee Code Quantity Amount Sub-Total in USD(5)  Saic Filing:  Sizellaneous-Filing:  Activition:  Activition:  Activity Amount Sub-Total in USD(5)	Electronic Patent Application Fee Transmittal								
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